

Abstract**Packet Switched Backplane**

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A packet switched backplane (100) having a plurality of node slots (102 through 106) wherein at least portion of said node slots comply with the PICMG 2.16 standard. At least one of said node slots (104, 106) complying with said PICMG 2.16 node standard is connected by means of dedicated links (124) to at least one another node slot (102) complying with said PICMG 2.16 node standard (aggregation slot).

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FIG. 1 to accompany the Abstract